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APPLICATION NO. FILING DATE		TE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/615,354 07/07/2003		03	Roy Alan Hastings	TI-35919	8886		
23494	7590 09	9/15/2006	EXAMINER				
TEXAS II	NSTRUMENTS 1	LE, DINH	LE, DINH THANH				
P O BOX 655474, M/S 3999 DALLAS, TX 75265				ART UNIT	PAPER NUMBER		
,				2816			
				DATE MAILED: 00/15/2006			

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No. Applicant(s)							
		10/615,354	4	HASTINGS, ROY ALAN					
Office Action Summary			Examiner		Art Unit				
		DINH T. LE		2816					
Period fo	The MAILING DATE of this commun or Reply	ication app	ears on the	cover sheet with the c	orrespondence ad	ldress			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
1)□	Responsive to communication(s) file	ed on							
	This action is FINAL . 2b)⊠ This action is non-final.								
3)□	,—								
•	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.								
Dispositi	on of Claims								
4)🖂	☑ Claim(s) <u>1-27</u> is/are pending in the application.								
•	4a) Of the above claim(s) is/are withdrawn from consideration.								
	Claim(s) is/are allowed.								
6)⊠	Claim(s) <u>1-27</u> is/are rejected.								
7)	Claim(s) is/are objected to.								
8)□	Claim(s) are subject to restriction and/or election requirement.								
Applicati	on Papers								
9)	The specification is objected to by th	e Examiner	r.						
10)	The drawing(s) filed on is/are:	: a)□ acce	epted or b)[objected to by the E	xaminer.				
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).									
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.									
Priority u	ınder 35 U.S.C. § 119								
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:									
	1. Certified copies of the priority documents have been received.								
	2. Certified copies of the priority documents have been received in Application No								
	3. Copies of the certified copies of the priority documents have been received in this National Stage								
application from the International Bureau (PCT Rule 17.2(a)).									
* See the attached detailed Office action for a list of the certified copies not received.									
Attachment(s)									
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date									
3) 🛛 Inform	nation Disclosure Statement(s) (PTO/SB/08) r No(s)/Mail Date <u>7/7/03, 9/27/04</u> .		5) Notice of Informal Pa						

DETAILED ACTION

Specification

The specification has been checked to the extent necessary to determine the presence of

all possible minor errors. However, the applicant's cooperation is requested in correcting any

errors of which applicant may become aware in the specification.

Claim Rejections

Claim Rejections - 35 USC § 112

Claims 1-27 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for

failing to particularly point out and distinctly claim the subject matter which applicant regards as

the invention. Correction or clarification is required.

In claim 1, it is unclear how the recitation "input transistors" and "circuit" on line 4 is

read on the preferred embodiment. Insofar as understood, no such elements are seen on the

drawings. Also, it is not understood how the translinear circuit can be "configured" to add the

currents since no means for performing the adding function is recited in this claim, how and

when the translinear circuit can operate within said at least one segment, how the input transistor

can be "responsible" for creating said removable singularity and how the singularity can be

created. The same is true for claim 11.

In claim 11, it is not understood how the translinear circuit can be "implemented" on line

1.

In claim 2, it is unclear "to what" the input current are proportional.

In claim 19, it is unclear how the region can be "detected", how the input currents can be "determined" and how the perturbation currents can be "defined", and how the recitation "steps of detecting, determining, defining and adding" is read on the preferred embodiment. Insofar as understood, no such steps are seen on the drawings.

In claims 24, it is unclear how the recitation "a pair of translinear loop", "current mirror" and "clamp transistor" is read on the preferred embodiment. Insofar as understood, no such elements are seen on the drawings.

In claim 25, it is unclear how the recitation "input transistors", "current mirror", "current source", "clamp transistor" and "control transistor" are read on the preferred embodiment.

Insofar as understood, no such elements are seen on the drawings.

In claim 26, it is not understood what the "limb" is. The recitation "a voltage rail" on lines 8 and 12 is confusing because it is unclear if this is additional "voltage rail" or further recitation of the previously claimed "voltage rail" on line 4. It is unclear how the recitation "first to sixth transistors", "current sources", "current mirror", "circuit output" and "clamp transistor" is read on the preferred embodiment or seen on the drawings.

The remaining claims are dependent from the above rejected claims and therefore also considered indefinite.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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Art Unit: 2816

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
 Claims 1-27 are rejected under 35 USC 102 (b) as being anticipated by Figures 2-3 of the applicant's admitted prior art.

As the best construed, the admitted prior art discloses in Figures 2-3 a circuit comprising:

- an input transistors (Q1, Q4) for receiving input currents (IA, IC);
- a current mirror (M1-M4, Figure 3);
- a current source (IX, Figure 3);
- a control transistor (Q10); and
- circuit for providing perturbation currents (16, 18, 20, Figure 2) or (24, 26) in Figure 3.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DINH T. LE whose telephone number is (571) 272-1745. The examiner can normally be reached on Monday-Friday (8AM-7PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY CALLAHAN can be reached at (571) 272-1740.

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Art Unit: 2816

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

13 September 2006

Primary Examiner